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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : **Confirmation No. 1525**  
Hiroyasu NODA : Attorney Docket No. 2003\_1734A  
Serial No. 10/724,045 : Group Art Unit 2181  
Filed December 1, 2003 : Examiner David E. Martinez

EXTENDING CIRCUIT FOR MEMORY AND: **Mail Stop Amendment**  
TRANSMITTING-RECEIVING DEVICE  
USING EXTENDING CIRCUIT FOR MEMORY

THE COMMISSIONER IS AUTHORIZED  
TO CHARGE ANY DEFICIENCY IN THE  
FEES FOR THIS PAPER TO DEPOSIT  
ACCOUNT NO. 23-0975

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

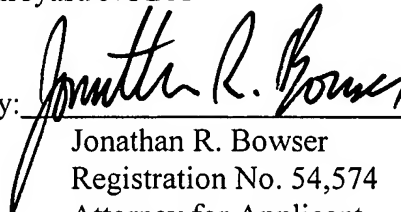
Sir:

In response to the restriction requirement contained on page 2 of the Office Action dated September 20, 2006, the Applicant hereby elects **Invention I**, which is drawn to an extending circuit for memory. Claims 5-7 read on the elected invention.

Having made the required election, a full examination on the merits is respectfully requested.

Respectfully submitted,

Hiroyasu NODA

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